

10/070313
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#44

IN THE UNITED STATES PATENT AND TRADE MARK OFFICE

In re U.S. Patent Application of)
ITO et al)
Application Number: To Be Determined)
Filed: Concurrently Herewith)
FOR: DATA PROCESSING DEVICE AND)
DATA PROCESSING SYSTEM)

Honorable Assistant Commissioner
for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Applicant has amended the claims in order to eliminate the multiple dependency of claims and thereby reduce the filing fee in accordance with standard U.S. practice. Prior to an examination on the merits, please amend the above-identified application as follows:

IN THE CLAIMS:

Please substitute the claims currently on file with the following amended claims:

(Amended) The data processing system according to claim 4, wherein the data processing device performs control in which data acquired in the burst operations for the single or plurality of times is filled in the cache memory according to the first information.

7. (Amended) The data processing system according to claim 4, wherein in cache fill operations, the data processing device generates a synchronization signal synchronous with a delimiter of data axquired from the memory in the burst operations, and the data processing device generates a cache-fill address for determining a data order of cache fill in synchronism with the synchronization signal in order to perform write control for the cache memory starting from the address information in a range of a burst length meat by

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